

**Latest Photo**

**Profile of Faculty for EE Deptt.**

**Name:** Hanuman

**Father’s Name:** Krishan Lal

**Qualifications:**

* B.Tech. (Electrical Engg.) from CDLM Govt. Engg. College, Panniwala Mota, Sirsa affiliated to KUK, Haryana.
* M.Tech. (Power System and Derives) from YMCAUST, Faridabad (Haryana)

**Area of Interest:**

* Power System**.**

**Experience:**

* Teaching: 05 Years
* Industry: 01 Years

 **List of Presentations in International/ National Conferences:(with**

 **complete detail):**

* Certify for Publishing a Paper titled **“Unbundling And Competition In Indian Electricity Market”** By **Authors:** Surender, Hanuman, Sita Devi, Ruby Sathiala, Guest Faculties, CDLSIET, Panniwala Mota (Sirsa) in **“International Journal For Technological Research In Engineering (IJTRE), Volume 5, Issue 10, June-2018.**
* Certify for Publishing a Paper titled **“Maximum Power Tracking For Photo-Voltaic Power System”** By **Authors:** Surender, Hanuman, Sita Devi, Ruby Sathiala, Guest Faculties, CDLSIET, Panniwala Mota (Sirsa) in **“International Journal For Technological Research In Engineering (IJTRE), Volume 5, Issue 10, June-2018.**

 **Responsibilities held:**

* AS in College in Semester Exam.
* Coordinators in Sports and Cultural Events.

**Achievements/ Awards, if any:**

* Nss Member at College Level.
* Team Member in Cricket and Hockey at School Level in Regoinal Game.
* Passed 22nd Annual All Indial UNESC Information Test.
* 2nd position in science quiz at school level.

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