

**Latest Photo**

**Profile of Faculty for EE Deptt.**

**Name:** Surender

**Father’s Name:** Shamsher Singh

**Qualifications:**

* B. Tech. (Electrical And Electronics Engg.) from JCDMCOE, Sirsa Affiliated to KUK, Haryana.
* M.Tech. (Electrical Power System) from GTC, Soldha, B.Garh (Jhajjar) Affiliated to MDU Rohtak, Haryana.

**Area of Interest:**

* Control System Engg.
* Power System Engg.
* Network Analysis.

**Experience:**

* Teaching: 04 Years.
* Industry: 01 Years.

**List of Presentations in International/ National Conferences: (with complete detail):**

* Certify for Publishing a Paper titled “**Simulation Study For Automatic Generation Control In the Restructured Power System Environment**” By **Author**: Surender (M.tech Scholar) with **Co-Author**: Pardeep Nain (Astt. Prof.) OITM, Juglan (Hisar) and Richa Bansal , Astt. Prof. GITAM , Kablana (Jhajjar) in International Conference held on “**Computer Applications in Manufacturing and Food Technologies And Bio-Nano Engineering”** **(CAMBTE-2014)** On Dated: 21-22 Feb, 2014 in UIT Hansi (Haryana).
* Certify for Publishing a Paper titled “**MATLAB Study For Automatic Generation Control In Deregulated Power System Environment**” By **Author**: Surender (M.tech Scholar) with **Co-Author**: Pardeep Nain (Astt. Prof.) OITM, Juglan (Hisar) and Joginder Singh, Astt. Prof. GITAM , Kablana (Jhajjar) in National Conference held on “**Recent Innovations in Engineeering, Management and Science” (RIEMS)** On Dated: 23 Aug, 2014 in GITAM, Kablana, Jhajjar (Haryana).
* Certify for Publishing a Paper titled **“Unbundling And Competition In Indian Electricity Market”** By **Authors:** Surender, Hanuman, Sita Devi, Ruby Sathiala, Guest Faculties, CDLSIET, Panniwala Mota (Sirsa) in **“International Journal For Technological Research In Engineering (IJTRE), Volume 5, Issue 10, June-2018.**
* Certify for Publishing a Paper titled **“Maximum Power Tracking For Photo-Voltaic Power System”** By **Authors:** Surender, Hanuman, Sita Devi, Ruby Sathiala, Guest Faculties, CDLSIET, Panniwala Mota (Sirsa) in **“International Journal For Technological Research In Engineering (IJTRE), Volume 5, Issue 10, June-2018.**

**Responsibilities held:**

* Worked as a Councillor in **“Budding Engineer’s Forum”.**
* Member of National and International Conference Organisation in UIT, Hansi (HR)
* Worked as a NSS Volunteers.
* Worked as a Cultural Coordinator.

**Achievements/ Awards, if any:**

* Cirtificate of Marit in NSS (KUK)
* Certificate of Appreciation in BEF.

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