Name of Faculty Discipline	:	Sahil Arora BTech ECE
Semester Subject	:	III Digital Electronics (ECE-209-L)
Lesson Plan Durati	on:	15 weeks per week (in hours): Lectures 04 hours.

Week		Theory	Actual Covered
	Lecture	Topic (Including Assignment/Test)	
. st	1	Digital signal, Error detection and correction codes.	
1 st	2	logic gates: AND, OR, NOT, NAND	
	3	NOR, EX-OR, EX-NOR	
	4	Boolean algebra	
	5	Review of Number systems	
2^{nd}	6	Binary codes: BCD, Excess-3, Gray	
	7	EBCDIC, ASCII	
	8	Error detection and correction codes	
	9	Design using gates	
3 rd	10	Karnaugh map	
	10	Problems on K map	
	11	Problems and Solutions on K map	
	12		
4^{th}		Quine Mcluskey methods of simplification	
т	14	Circuit desig using gates, adder, subtractor, comparator	
	15	BCD to seven segmant, code converters	
	16	MUX and DEMUX :use as logic elements	
- 1	17	Decoders, Encoder	
5th	18	Adders / Subtractors	
	19	BCD arithmetic circuits	
	20	Flip Flops : S-R, J-K	
	21	T, D ff	
6th	22	master-slave, edge triggered, flip flop conversion	
	23	shift registers, bidirectional shift registers	
	24	sequence generators	
7 th		Ist Minor Test	
,	25	Ring Counters	
8th	26	Johnson Counter	
	27	Design of Synchronous and Asynchronous sequential circuits	
	28	Assignment questions	
	20	Finite state Machines-Timing Diagram, Moorey vs Mealy	
9th	30		
<i>y</i> th		FSM design procedure, state diagram	
	31	State transition table, state minimization	
	32	State encoding, next state logic minimization	
10/1	33	Implement the design	
10th	34	Problems and Solutions	
	35	Switching mode operation of p-n junction	
	36	bipolar and MOS. devices	
	37	Bipolar logic families:RTL, DTL, DCTL	
4	38	HTL, TTL, ECL, MOS	
11^{th}	39	CMOS logic families	
	40	Tristate logic	
	41	Interfacing of CMOS and TTL families.	
12th	42	Sample and hold circuit	
	43	weighted resistor and R -2 R ladder D/A Converter	
	44	specifications for D/A converters. A/D converters : Quantization, parallel -	
	44		
13 th	45	comparator successive approximation type	
15	45		
		counting type, dual-slope ADC, specifications of ADCs	
	47	ROM	
	48	PLA, PAL	
4th		2 nd Minor Test	
	49	FPGA	
15th	50	Assignment Evaluation	
	51	CPLDs	
	52	Implementation of Combinational circuit using ROM, PLA, PAL	

Name of Faculty	:	Vikram Singh Bhambhu, Guest Lecturer(ECE)		
Discipline	:	Electronics & Communication Engg.		
Semester	:	3 rd		
Subject	:	Analog Electronics		
Lesson Plan Durat	ion:	15 weeks (from August, 2018 to December, 2018)		
Work Load (Lectutre/Practical) per week (in hours): Lectures 04 hours				

Week		Theory
	Lecture	Topic
	Day	
1 st	1	Introduction to Analog electronics
1 ^{sx}	2	P-N junction theory
	3	P-N junction V-I Characteristics
	4	P-N junction as a rectifier
- nd	5	Switching characteristics of Diode
2^{nd}	6	Diode as a circuit element
	7	The load-line concept
	8	Half-wave and full wave rectifiers
. ed	9	Clipping circuits
3 rd	10	Clamping circuits
	11	Filter circuits
	12	Peak to peak detector
.1	13	Voltage multiplier circuits
4^{th}	14	Bipolar junction transistor operation
	15	Bipolar junction transistor characteristics
	16	Ebers-moll model of transistor
	17	Hybrid model
5^{th}		
	18	H-parameters (CE, CB, CC configurations)
	19	Analysis of a transistor amplifier circuits using h-parameters
	20	Emitter follower
	21	Miller's Theorem
6 th	22	Frequency response of R-C coupled amplifier
	23	Operating point
	24	Bias stability
7 th		
		Minor Test 1
oth	25	Collector to base bias
8 th	26	Collector to base bias Self-bias
8 th	26 27	Collector to base bias Self-bias Emitter bias
8 th	26 27 28	Collector to base bias Self-bias Emitter bias Bias compensation
	26 27 28 29	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation
8 th 9 th	26 27 28 29 30	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation
	26 27 28 29 30 31	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model
	26 27 28 29 30 31 32	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain
9 th	26 27 28 29 30 31 32 33	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response
	26 27 28 29 30 31 32 33 34	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha
9 th	26 27 28 29 30 31 32 33 34 35	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency
9 th	$ \begin{array}{r} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ \end{array} $	Collector to base biasSelf-biasEmitter biasBias compensationSensistor compensationThermistor compensationHybrid P modelCE short circuit current gainFrequency responseAlphaCutoff frequencyGain bandwidth product
9 th	26 27 28 29 30 31 32 33 33 34 35 36 37	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies.
9 th	26 27 28 29 30 31 32 33 34 35 36 37 38	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor
9 th	26 27 28 29 30 31 32 33 34 35 36 37 38 39	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage
9 th 10 th	26 27 28 29 30 31 32 33 34 35 36 37 38	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor
9 th	26 27 28 29 30 31 32 33 34 35 36 37 38 39	Collector to base biasSelf-biasEmitter biasBias compensationSensistor compensationThermistor compensationHybrid P modelCE short circuit current gainFrequency responseAlphaCutoff frequencyGain bandwidth productEmitter follower at high frequencies.Junction field effect transistorPinch off voltageVolt-ampere characteristics
9 th 10 th	$\begin{array}{c} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ 37 \\ 38 \\ 39 \\ 40 \\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small I signal model
9 th 10 th	$\begin{array}{c} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ 37 \\ 38 \\ 39 \\ 40 \\ 41 \\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode
9 th 10 th	$\begin{array}{c} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ 37 \\ 38 \\ 39 \\ 40 \\ 41 \\ 42 \\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small I signal model
9 th 10 th 11 th 12 th	$\begin{array}{c} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ 37 \\ 38 \\ 39 \\ 40 \\ 41 \\ 42 \\ 43 \\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode
9 th 10 th	$\begin{array}{c} 26 \\ 27 \\ 28 \\ 29 \\ 30 \\ 31 \\ 32 \\ 33 \\ 34 \\ 35 \\ 36 \\ 37 \\ 38 \\ 39 \\ 40 \\ 41 \\ 42 \\ 43 \\ 44 \\ \end{array}$	Collector to base biasSelf-biasEmitter biasBias compensationSensistor compensationThermistor compensationHybrid P modelCE short circuit current gainFrequency responseAlphaCutoff frequencyGain bandwidth productEmitter follower at high frequencies.Junction field effect transistorPinch off voltageVolt-ampere characteristicsSmall 1 signal modelMOSFET Enhancement modeV-MOSFETCommon source amplifier
9 th 10 th 11 th 12 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ 46\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode V-MOSFET Common source amplifier
9 th 10 th 11 th 12 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode V-MOSFET Common source amplifier Source follower Biasing of FET
9 th 10 th 11 th 12 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ 44\\ 45\\ 46\\ 47\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode V-MOSFET Common source amplifier
9 th 10 th 11 th 12 th 13 th 14 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ 44\\ 45\\ 46\\ 47\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode V-MOSFET Common source amplifier Source follower Biasing of FET Applications of FET as a voltage variable resistor (V V R).
9 th 10 th 11 th 12 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ 44\\ 45\\ 46\\ 47\\ 48\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Depletion mode V-MOSFET Common source amplifier Source follower Biasing of FET Applications of FET as a voltage variable resistor (V V R). Minor Test 2
9 th 10 th 11 th 12 th 13 th 14 th	$\begin{array}{c} 26\\ 27\\ 28\\ 29\\ 30\\ 31\\ 32\\ 33\\ 34\\ 35\\ 36\\ 37\\ 38\\ 39\\ 40\\ 41\\ 42\\ 43\\ 44\\ 45\\ 44\\ 45\\ 46\\ 47\\ 48\\ 48\\ 49\\ 49\\ \end{array}$	Collector to base bias Self-bias Emitter bias Bias compensation Sensistor compensation Thermistor compensation Hybrid P model CE short circuit current gain Frequency response Alpha Cutoff frequency Gain bandwidth product Emitter follower at high frequencies. Junction field effect transistor Pinch off voltage Volt-ampere characteristics Small 1 signal model MOSFET Enhancement mode MOSFET Depletion mode V-MOSFET Common source amplifier Source follower Biasing of FET Applications of FET as a voltage variable resistor (V V R).

Vikram Singh Bhambhu

Guest Lecturer (ECE)

Lesson Plan

Name of FacultyPoonamDisciplineBTech CSESemesterIIISubjectDigital Electronics (EE-204-E)Lesson Plan Duration:15 weeksWork Load (Lecture/Practical) per week (in hours): Lectures 04 hours.

Week		Theory	Actual Covered
	Lecture Day	Topic (Including Assignment/Test)	
	1	Digital signal, EBCDIC, ASCII, Error detection and correction codes.	
1^{st}	2	logic gates: AND, OR, NOT, NAND	
	3	NOR, EX-OR, EX-NOR	
	4	Boolean algebra	
	5	Review of Number systems	
2^{nd}	6	Binary codes: BCD, Excess-3, Gray	
	7	EBCDIC, ASCII	
	8	Error detection and correction codes	
	9	Design using gates	
3 rd	10	Karnaugh map	
	11	Problems on K map	
	12	Problems and Solutions on K map	
	13	Quine Mcluskey methods of simplification	
4^{th}	14	Problems on Quine Mcluskey methods of simplification	
	15	Discussion on K map and Quine Mcluskey methods of	
		simplification	
	16	Multiplexers	
	17	Demultiplexers	
5th	18	MUX and DEMUX :use as logic elements	
	19	Decoders	
-	20	Adders / Subtractors	
	21	BCD arithmetic circuits	
6th	22	Encoder	
	23	Decoders / Drivers for display devices	
	24	Problems and Solutions	
7 th		Ist Minor Test	
0.1	25	Flip Flops : S-R, J-K	
8th	26	T, D ff	
	27	master-slave, edge triggered	
	28	shift registers,	
	29	Assignment questions	
9th	30	sequence generators	
	31	Counters	
	32	Asynchronous and Synchronous Ring counters	
	33	Johnson Counter	
10th	34	Design of Synchronous and Asynchronous sequential circuits	
	35	Switching mode operation of p-n junction	

	36	bipolar and MOS. devices	
	37	Bipolar logic families:RTL, DTL, DCTL	
1.1 th	38	HTL, TTL, ECL, MOS	
11 th	39	CMOS logic families	
	40	Tristate logic	
	41	Interfacing of CMOS and TTL families.	
12th	42	Sample and hold circuit	
	43	weighted resistor and R -2 R ladder D/A Converter	
	44	specifications for D/A converters. A/D converters : Quantization,	
		parallel - comparator	
13 th	45	successive approximation type	
	46	counting type, dual-slope ADC, specifications of ADCs	
	47	ROM	
	48	PLA, PAL	
		2 nd Minor Test	
14th			
	49	FPGA	
15th	50	Assignment Evaluation	
	51	CPLDs	
	52	Problems and Solutions	

ber, 2018)
5.

Week		Theory
	Lecture	Topic (Including Assignment/Test)
	Day	
		Unit1
	1	Introduction & Application of instrument system
1^{st}	2	Functional Element of a Measurement System
	3	Classification of instruments, Standard & Calibration
	4	Static & Dynamic Characteristics of Inst., Precision & Accuracy.
2^{nd}	5	Resolution, Threshold, Sensitivity, Linearity
	6	Hysteresis, Dead Band, backlash, Drift
	7	Formulation of Differential Equation
3^{rd}	8	Zero, First and Second Order System
	9	Response of First & Second Order System to Step, Ramp Fn.
	10	Response of First & Second Order System to Impulse and Harmonic
4^{th}	11	Unit-II Block Diagram of Oscilloscope & Study of Various stages
	12	High Freq CRO & Sampling & Storage Oscilloscope
	13	Measurement of Phase & Freq.
5^{th}	14	DC & AC voltage Measurement
	15	DC & AC current Measurement & Assignment 1
	16	Multi-meter, Ohmmeter & Bolometer
6^{th}	17	Calorimeter & Power meter
	18	Introduction to Digital Meter
7^{th}		1 st Minor Test
		Unit 3
	19	Block Diagram of Pulse Generator
8^{th}	20	Signal & Function Generator
	21	Wave analyser
	22	Distortion & Specturm analyser
9^{th}	23	Harmonic analyser
	24	Power Analyser
	25	Study of Decade Counting Assembly
10^{th}	26	Freq. & Period Measurement
	27	Universal Counter & Introduction to Digital Meter
		Unit 4
	28	Classification of Transducer

11 th	29	RLC & Photocell Transducer
	30	Measurement of Displacement & Velocity
12^{th}	31	Measurement of Acceleration
	32	Measurement of Strain & Pressure
	33	Measurement of Liquid Level & Temp.
	34	DC signal Conditioning System
13 th	35	AC signal Conditioning System
	36	Data Acquisition & Conversion system
14th		2 nd Minor Test
	37	Characteristics of Modern digital Acquisition System Amplifier
15^{th}		Characteristics
	38	Filter & Assignment 2
	39	Settling time & Amplifier Characteristics

Gourav Sharma Guest Faculty E.C.E.Department

Name of Faculty	:	Gourav Sharma, Guest Faculty
Discipline	:	Electronics & Communication Engg.
Semester	:	7th
Subject	:	Optical Communication (EE-405E)
Lesson Plan Durat	ion:	15 weeks (from August, 2018 to November, 2018)
Work Load (Lecturer) :		Per week (in hours): Lectures 04 hours.

Week		Theory
	Lecture I	Day Topic (Including Assignment/Test)
	1	Electromagnetic spectrum used for optical communication
1^{st}	2	Block diagram of optical communication system
	3	Basics of transmission of light rays
	4	Advantages of optical fiber communication
	5	Optical fibers structures and their types
2^{nd}	6	Attenuation
	7	Scattering
	8	Absorption
	9	Fiber Bend Loss
3 rd	10	Dispersion
	11	Fiber Coupler
	12	Connector
4 b	13	Problem & Solution of 1 st Unit
4^{th}	14	Problem & Solution of 2 nd Unit
	15	Light emitting diode
	16	Recombination processes
th	17	The spectrum of recombination radiation
5 th	18	LED characteristics
	19	Internal quantum efficiency
	20	External quantum efficiency
-th	21	LED structure
6^{th}	22	Lens coupling to fiber
	23	Behavior at high frequencies.
	24	Problem & Solution of 3 rd Unit
7th	1 st Minor Test	
oth		Basic principles of laser action in semi -conductors
8 th		optical gain,
		lasing threshold
	28	Assignment 1
9 th		laser structures and characteristics
9		laser to fiber coupling
	31	Comparison with LED source.
	32	Problems and Solution of 4 th Unit

33	Principles of optical detection		
34	Quantum efficiency, Responsivity		
35	General principles of PIN photodetector		
36	Intrinsic absorption		
37	Materials and designs for PIN photodiodes, ,		
38	Impulse and frequency response of PIN photodiodes		
39	Noise in PIN Photodiodes		
40	Problems and Solution		
41	multiplication process		
42	APD Design,		
43	APD bandwidth,		
44	APD noise		
45	Assignment-II		
46	Revision & Problem Solving of 1 st Unit		
47	Revision & Problem Solving of 2 nd Unit		
48	Revision & Problem Solving of 2 nd Unit		
2 nd Mino	r Test		
49	Revision & Problem Solving of 3 rd Unit		
50	Revision & Problem Solving of 4 th Unit		
51	Revision & Problem Solving of 5 th Unit		
52	Revision & Problem Solving of 5 th Unit		
	34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 2 nd Mino 49 50 51		

Name of Faculty	:	Rupinder Kaur, Assistant Professor
Discipline	:	ECE
Semester	:	IIIrd
Subject	:	SIGNALS AND SYSTEMS (ECE-203-L)
Lesson Plan Durati	on:	15 weeks (From July 2018 to Dec. 2018)
Work Load (Lectutre/Practical) per week (in hours): Lectures: 04		

Week	Theory	
	Lecture	Topic (Including Assignment/Test)
	Day	
	1	Signal Definition
1^{st}	2	Classification of Signals
	3	Basic/Singularity
	4	Continuous and Discrete time signals
	5	Basic operations of signals
2^{nd}	6	Time Shifting
	7	Time Reversel
	8	Time Scaling
	9	Signal Representation in terms of singular functions
3 rd	10	Correlation of Signals
	11	Correlation Properties
	12	Representation of a continuous time signal by samples
	13	The Sampling Theorem
4^{th}	14	Reconstruction of signal from its samples
	15	Aliasing
	16	Introduction to Systems
	17	Classification of Systems
5th	18	Linear and Non linear Systems
	19	Static and Dynamic Systems
	20	Casual & Non-casual Systems
	21	Invertible & Non invertible Systems
6th	22	Stable & Unstable Systems
	23	Time variant & Time invariant Systems
	24	Assignment I
7th		Minor Test II
	25	Introduction to Linear Time invariant Systems
8th	26	Properties of LTI
	27	Convulation Sum/Integral and its Properties
	28	Representation of LTI systems using Differential equations
	29	Representation of LTI systems using Difference equations
9th	30	Introduction to Frequency Domain Representation
	31	Fourier Series Representation of Periodic Signals

	32	Convergence of Fourier Transform
	33	Properties of Fourier Series
10th	34	Fourier Transform for Periodic Signals
	35	Fourier Transform for Aperiodic Signals
	36	Convergence of Fourier Transform
	37	Properties of Fourier Transform
11th	38	Applications of Fourier Transform
	39	Introduction to Discrete - Time Fourier Transform
	40	Fourier Transform representation for Discrete - Time Aperiodic
		Signals
12th	41	Fourier Transform representation for Discrete - Time Periodic
		Signals
	42	Properties of Discrete - Time Fourier Transform
	43	Basic Fourier Transform Pairs
	44	Introduction to Z - Transform
	45	Region of Convergence for Z - Transform
13th	46	Assignment II
	47	Z – Transform Properties
	48	Problems and Solutions
14th	Minor Test II	
	49	Analysis of LTI Systems using Z – Transform
15th	50	Applications of Z - Transform
	51	Inverse Z – Transform
	52	Introduction to Hilbert Transform

Name of Faculty :	SAHIL ARORA, Assistant Professor (ECE)
Discipline	: ECE 3RD SEM
Subject :	DIGITAL ELECTRONICS (ECE-209-L)
Lesson Plan Duration:	15 weeks (From July 2018 to Dec. 2018) 4 LECT PER WEEK.

Week	Theory	
	Lecture	Topic (Including Assignment/Test)
	1	Digital signal, Error detection and correction codes.
1 st	2	logic gates: AND, OR, NOT, NAND
	3	NOR, EX-OR, EX-NOR
	4	Boolean algebra
	5	Review of Number systems
2 nd	6	Binary codes: BCD, Excess-3, Gray
	7	EBCDIC, ASCII
	8	Error detection and correction codes
	9	Design using gates
3 rd	10	Karnaugh map
	11	Problems on K map
	12	Problems and Solutions on K map
	13	Quine Mcluskey methods of simplification
4 th	14	Circuit desig using gates, adder, subtractor, comparator
	15	BCD to seven segmant , code converter\
	16	MUX and DEMUX :use as logic elements
	17	Decoders, Encoder
5th	18	Adders / Subtractors
	19	BCD arithmetic circuits

	20	
	20	Flip Flops : S-R, J-K
	21	T, D ff
6th	22	master-slave, edge triggered, flip flop conversion
	23	shift registers, bidirectional shift registers
	24	sequence generators
7 th		Ist Minor Test
	25	Ring Counters
8th	26	Johnson Counter
	27	Design of Synchronous and Asynchronous sequential circuits
	28	Assignment questions
	29	Finite state Machines-Timing Diagram, Mooreyvs Mealy
9th	30	FSM design procedure, state diagram
	31	State transition table, state minimization
	32	State encoding, next state logic minimization
	33	Implement the design
10th	34	Problems and Solutions
	35	Switching mode operation of p-n junction
	36	bipolar and MOS. devices
	37	Bipolar logic families:RTL, DTL, DCTL
	38	HTL, TTL, ECL, MOS
11 th	39	CMOS logic families
	40	Tristate logic
	41	Interfacing of CMOS and TTL families.
12th	42	Sample and hold circuit
	43	weighted resistor and R -2 R ladder D/A Converter
	44	specifications for D/A converters. A/D converters : Quantization, parallel - comparator
13 th	45	successive approximation type
	46	counting type, dual-slope ADC, specifications of ADCs
	47	ROM
	48	PLA, PAL
14th		2 nd Minor Test
	49	FPGA
15th	50	Assignment Evaluation
	51	CPLDs
	52	Implementation of Combinational circuit using ROM,PLA,PAL

Name of Faculty :	RupinderKaur, Assistant Professor (ECE)
Discipline	: ECE 3 RD SEM
Subject :	SIGNALS AND SYSTEMS (ECE-203-L)
Lesson Plan Duration:	15 weeks (From July 2018 to Dec. 2018) 4 LECT PER WEEK.

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Week	Theory	
	Lecture Day	Topic (Including Assignment/Test)
	1	Signal Definition
1 st	2	Classification of Signals
	3	Basic/Singularity
	4	Continuous and Discrete time signals
	5	Basic operations of signals
2 nd	6	Time Shifting
	7	Time Reversel
	8	Time Scaling
	9	Signal Representaion in terms of singular functions
3 rd	10	Correlation of Signals

	11	Correlation Propertie
	12	Representation of a continuous time signal by samples
	13	The Sampling Theorem
4 th	_	
4	14	Reconstruction of signal from its samples
	15	Aliasing
	16	Introduction to Systems
	17	Classification of Systems
5th	18	Linear and Non linear Systems
	19	Static and Dynamic Systems
	20	Casual & Non-casual Systems
	21	Invertible & Non invertible Systems
6th	22	Stable & Unstable Systems
	23	Time variant & Time invariant Systems
	24	Assignment I
7th		Minor Test II
	25	Introduction to Linear Time invariant Systems
8th	26	Properties of LTI
	27	Convulation Sum/Integral and its Properties
	28	Representation of LTI systems using Differential equations
	29	Representation of LTI systems using Difference equations
9th	30	Introduction to Frequency Domain Representaion
	31	Fourier Series Representation of Periodic Signals
	32	Convergence of Fourier Transform
404	33	Properties of Fourier Series
10th	34	Fourier Transform for Periodic Signals
	35	Fourier Transform for Aperiodic Signals
	36	Convergence of Fourier Transform
	37	Properties of Fourier Transform
11th	38	Applications of Fourier Transform
	39	Introduction to Discrete -Time Fourier Transform
	40	Fourier Transform representation for Discrete - Time Aperiodic Signals
12th	41	Fourier Transform representation for Discrete - Time Periodic Signals
	42	Properties of Discrete -Time Fourier Transform
	43	Basic Fourier Transform Pairs
	44	Introduction to Z - Transform
	45	Region of Convergence for Z - Transform
13th	46	Assignment II
	47	Z – Transform Properties
	48	Problems and Solutions
14th		
	49	Analysis of LTI Systems using Z – Transform
15th	50	Applications of Z - Transform

51	Inverse Z – Transform
52	Introduction to Hilbert Transform

LESSON PLAN

Name of Faculty : Poonam

Discipline :BTech ECE 3RD SEM

Subject : Network analysis and synthesis (ECE-207-L)

Lesson Plan Duration :15 weeksWork Load (Lecture/Practical) per week (in hours): Lectures 04 hours

Week	Theory	
	Lecture Day	Topic (Including Assignment/Test)

	1	Introduction to laplace transformation
1 st	2	Properties of Laplace transformation
	3	Laplace transform of special signal waveforms
	4	Inverse laplace transform
	5	Use of laplace transform in solving electrical networks
2 nd	6	Problems and solution on laplace transform
	7	Initial conditions of resistive, inductive and capacitive elements
	8	Time domain analysis of simple linear circuits
3 rd	9	Transient and steady state response of RC,RL,RLC Circuit to step signal using laplace transform
5	10	Transient and steady state response of RC,RL,RLC Circuit to ramp signal using laplace transform
	11	Transient and steady state response of RC,RL,RLC Circuit to impulse signal using laplace transform
	12	Transient and steady state response of RC,RL,RLC Circuit to sinusoidal signal using laplace transform
	13	Problems and solutions
4 th	14	Terminal pairs or Ports
	15	Network functions for one-port and two-port networks
	16	poles and zeros of Network functions
5th	17	Demultiplexers Restrictions on pole and zero Locations for driving point functions and transfer functions
Still	18	Time domain behavior from the pole-zero plot.
	19	Problems and solution on Time domain behavior from the pole-zero plot.
	20	Relationship of two-port variables
	21	short-circuit Admittance parameters, open circuit impedance, parameters
6th	22	Transmission parameters, hybrid parameters
	23	Relationships between parameter sets
	24	Inter-connection of two port networks.
7 th		Ist Minor Test
	25	Concept of network graph
8th	26	Terminology used in network graph
	27	Relationship between twigs and links
	28	Properties of tree in a graph
	29	Assignment questions
9th	30	Formation of incidence matrix,number of trees in a graph
	31	Cut set matrix
	32	Tie set matrix
	33	Formulation of network equilibrium equation
10th	34	network analysis using graph theory
	35	Intoduction to filters, characteristics of filters
	36	bipolar and MOS. devices
	37	Bipolar logic families:RTL, DTL, DCTL
	38	Passive filter:HPF

11 th	39	LPF
	40	BPF
	41	BSF
12th	42	Introduction to m derived filters
	43	Introduction to active filters
	44	Concept and significance of positive real functions
13 th	45	Concept of network synthesis
	46	Driving point immittance function structure of RC Network
	47	Driving point immittance function structure of RL Network
	48	Foster form of RCnetwork
4 444		2 nd Minor Test
14th		
	49	Foster form of RCnetwork
15th	50	Assignment Evaluation
	51	CAUER form of RCnetwork
	52	CAUER form of RCnetwork

Name of Faculty :	Ms.Poonam, Assistant Professor of ECE
Discipline	:ECE 3 RD SEM
Subject :	Network analysis and synthesis lab(ECE-207P)
Lesson Plan Duration	:15 weeks (from august-2018 to 14ecember-2

:15 weeks (from august-2018 to 14ecember-2018)Practical-02 hours

Week	Practical (Group-I/ II)					
	Practical Day	Topics/ Programs				
1 st	1	Transient Response of RC circuit				
2 nd	2	Transient Response of RI circuit				
3 rd	3	To find the resonance frequency, Bandwidth of RLC series circuit				
4 th	4	To calculate and verify z parameters of two port network				
5 th	5	To calculate and verify y parameters of two port network				
6 th	6	Internal 1 st viva – voce				
7 th	IST MINOR TEST					
8 th	7	To calculate and verify ABCD parameters of two port network				
9 th	8	To calculate and verify H parameters of two port network				
10 th	9	To determine equivalent parameter of parallel connections of two port network				
11 th	10	To plot the frequency response of LPF and determine half power freq				
12 th	11	To plot the frequency response of HPF and determine half power freq				
13 th	12	To plot the frequency response of BPF and determine tha bandwidth				
14 th	2 ND MINOR TEST					
15 th	13	To synthesise a network of a given network function and verify its response				

Name of Faculty : Vikram Singh Bhambhu, ASSISTANT PROF (ECE) Discipline ECE 3RD : Subject : **Analog Electronics**

Week	Theory	
	Lecture Day	Торіс
	1	Introduction to Analog electronics
1 st	2	Conductivity of semiconducator
	3	Carrier concentration in an intrinsic semiconductor
	4	Fermilevel in intrinsic and extrinsic semiconductor
	5	Carrier life time
2 nd	6	Continuity Equation
	7	Hall effect
	8	Qualitative theory of pn junction
	9	Pn junction as adiode
3 rd	10	Band structure of an open circuited pn junction
	11	Current component in pn diode
	12	PN diode switching time, tunnel diode
	13	Rectifier with filter circuit
4 th	14	BJT construction opertation
	15	Characterstics ,Ebbers moll model
	16	BJT as an amplifier and switch
	17	Limits of operation, thermal runway
5 th	18	Stability factor bias stability of self bias
	19	Emmiter bias , collector to base bias
	20	Bias compensation
	21	Thermistor and sensistor
6 th	22	AC and DC Load linefor an amplifier
	23	Transistor hybrid model
	24	H parameter
7 th		
-		Minor Test 1
	25	Analysis of Transistor using h parameter
8 th	26	Symplified CE hybrid model
	27	Frequency response of RC coupled Amplifier
	28	MOSFET :Review of device structure
	29	Operation JFET
9 th	30	V-I Characterstics JFET
	31	Operation MOSFET
	32	V-I Characterstics MOSFET
	33	MOSFET as an Switch and amplifier FET small signal model
10 th	34	V MOSFET
	35	Common source apmlifier
	36	Source follower

	37	Biasing the FET				
11 th	38	FET as Voltage Vrariable Resistor				
	39	Miller Theorem				
	40	Hybrid pi model				
12 th	41	CE short circuit current gain				
	42	Frequency Response				
	43	Beta cut-off frequency				
	44	Gain bandwidth product				
	45	Series regulator				
13 th	46	Shunt regulator				
	47	Three terminal fixed IC Regulator 78xx/79xx				
	48	Adjustable voltage regulator				
14 th	Minor	Test 2				
	49	SMPS				
15 th	50	Revision				
	51	Revision				
	52	Revision				

Name of Faculty	:	Poonam
Discipline	:	BTech ECE
Semester	:	Ш
Subject	:	Network analysis and synthesis (ECE-207-L)
Lesson Plan Duration	:	15 weeks

Work Load (Lecture/Practical) per week (in hours): Lectures 04 hours.

Week	Theory				
	Lecture Day	Topic (Including Assignment/Test)			
	1	Introduction to laplace transformation			
1^{st}	2	Properties of Laplace transformation			
	3	Laplace transform of special signal waveforms			
	4	Inverse laplace transform			
	5	Use of laplace transform in solving electrical networks			
2^{nd}	6	Problems and solution on laplace transform			
	7	Initial conditions of resistive, inductive and capacitive elements			
	8	Time domain analysis of simple linear circuits			
	9	Transient and steady state response of RC,RL,RLC Circuit to step			
3^{rd}		signal using laplace transform			
	10	Transient and steady state response of RC,RL,RLC Circuit to			
		ramp signal using laplace transform			
	11	Transient and steady state response of RC,RL,RLC Circuit to			
		impulse signal using laplace transform			
	12	Transient and steady state response of RC,RL,RLC Circuit to			
		sinusoidal signal using laplace transform			
	13	Problems and solutions			
4^{th}	14	Terminal pairs or Ports			
	15	Network functions for one-port and two-port networks			
	16	poles and zeros of Network functions			
	17	Demultiplexers Restrictions on pole and zero Locations for			
5th		driving point functions and transfer functions			
	18	Time domain behavior from the pole-zero plot.			
	19	Problems and solution on Time domain behavior from the pole- zero plot.			
	20	Relationship of two-port variables			
6th	21	short-circuit Admittance parameters, open circuit impedance, parameters			

	22	Transmission parameters, hybrid parameters					
	23	Relationships between parameter sets					
	24	Inter-connection of two port networks.					
7 th		Ist Minor Test					
	25	Concept of network graph					
8th	26	Terminology used in network graph					
	27	Relationship between twigs and links					
	28	Properties of tree in a graph					
	29	Assignment questions					
9th	30	Formation of incidence matrix, number of trees in a graph					
-	31	Cut set matrix					
	32	Tie set matrix					
	33	Formulation of network equilibrium equation					
10th	34	network analysis using graph theory					
	35	Intoduction to filters, characteristics of filters					
	36	bipolar and MOS. devices					
	37	Bipolar logic families:RTL, DTL, DCTL					
	38	Passive filter:HPF					
th	39	LPF					
11 th	40	BPF					
	41	BSF					
12th	42	Introduction to m derived filters					
	43	Introduction to active filters					
	44	Concept and significance of positive real functions					
13 th	45	Concept of network synthesis					
	46	Driving point immittance function structure of RC Network					
	47	Driving point immittance function structure of RL Network					
	48	Foster form of RCnetwork					
		2 nd Minor Test					
14th							
	49	Foster form of RCnetwork					
15th	50	Assignment Evaluation					
	51	CAUER form of RCnetwork					
	52	CAUER form of RCnetwork					

Name of Faculty	:	Mr. Pramod Lega, Assistant Professor
Discipline	:	Management
Semester	:	3rd
Subject	:	Personality Development (PSY-201-L)
Lesson Plan Durati	ion:	15 weeks (from August, 2018 to November, 2018)

Work Load (Lecture/Practical) per week (in hours): Lectures 03 hours.

Lecture				
	y Y			
Day				
1	Introduction of Self			
	Meaning and Definitions of Self			
	Meaning and Definitions of Self-Esteem			
	Importance of Self-Esteem			
5	Characteristics of individuals with high self-esteem			
6	Characteristics of individuals with low self-esteem			
	Meaning and Definitions of Self- Confidence			
8	Strategies of building self-confidence			
9	Case Study			
10	Problems and Solutions			
11	Meaning and Definitions of Personality			
12	Problems and Solutions			
13	Factors affecting Personality			
14	Biological Factors			
15	Psychological Factors			
16	Social Factors			
17	Theories of Personality			
18	Type And Trait Theories (Case Study)			
	Ist Minor Test			
19	Freud's Theory of Personality			
20	Allport's Theory of Personality			
21	Assessment- Neo-Big Five Personality Test			
22	Thematic Apperception Test (T.A.T)			
23	Word Association Test (Case Study)			
24	Play Technique (Case Study)			
25	Dramatic Production Test (Čase Study)			
26	Verbal Projection Test (Case Study)			
27	Problems and Solutions			
28	Meaning and Definitions of Stress			
29	Causes of Stress and its impact,			
30	Strategies of stress management			
31	Case study			
	Problems and Solutions			
	Meaning and Definitions of Emotional Intelligence			
	Concept, emotional quotient why Emotional Intelligence matters			
	Measuring EQ			
	Developing healthy emotions			
	2 nd Minor Test			
37	Management of anger and interpersonal relations.			
	Case study.			
	Problems and Solutions			
	$\begin{array}{c} 2\\ 2\\ 3\\ 4\\ 5\\ 6\\ 7\\ 8\\ 9\\ 10\\ 11\\ 12\\ 13\\ 14\\ 15\\ 16\\ 17\\ 18\\ 14\\ 15\\ 16\\ 17\\ 18\\ 20\\ 21\\ 22\\ 23\\ 24\\ 25\\ 26\\ 27\\ 28\\ 29\\ 30\\ \end{array}$			

Name of Faculty	:	Dr. Meena Kumari			
Discipline	:	Applied Sciences & Humanities			
Semester	:	3rd			
Subject	:	Personality Development (PSY-201-L)			
Lesson Plan Durat	on:	15 weeks (August 18 to December 2018)			
Work Load (Lecture	Work Load (Lecture/Practical) per wook (in hower) a set				

Work Load (Lecture/Practical) per week (in hours): Lectures 03 hours.

Week		Theory T			
	Lecture Day	Topic (Including Assignment/Test)		1	
1-	1	Introduction of Self			
	2	Meaning and Definitions of Self			
	3	Meaning and Definitions of Self-Esteem			
	4	Importance of Self-Esteem			
2*	5	Characteristics of individuals with high self-estern			
	6	Characteristics of individuals with low self-esteen			
	7	Meaning and Definitions of Self-Confidence			
3~	8	Strategies of building self-confidence			
	9	Case Study			
	10	Problems and Solutions			
4ª	11	Meaning and Definitions of Personality			
	12	Problems and Solutions			
	13	Factors affecting Personality			
5th	14	Biological Factors			
	15	Psychological Factors			
	16	Social Factors			
6 th	17	Theories of Personality			
~	18	Type And Trait Theories (Case Study)			
7 *		Ist Minor Test			
0.1	19	Freud's Theory of Personality			
8th	20	Allport's Theory of Personality			
	21	Assessment- Neo-Big Five Personality Test	5		
9th	22	Thematic Apperception Test (T.A.T)			
ЭЩ	23	Word Association Test (Case Study)		g = M - 1 - 1	
	24	Play Technique (Case Study)	1		
10th	25	Dramatic Production Test (Case Study)			
	26	Verbal Projection Test (Case Study)			
	28	Problems and Solutions			
114	29	Meaning and Definitions of Stress			
	30	Causes of Stress and its impact,			
······································	31	Strategies of stress management			
12*	32	And a second sec			
	33	Problems and Solutions			
13*	34	Meaning and Definitions of Emotional Intelligence	.*		
	35	Concept, emotional quotient why Entotional Intelligence Measuring EQ			
	36				1
]4•		Developing healthy emotions		·	1
1	37	Management of anger and interpersonal relations.	/	a sa ka)
15*	38	Case study.			ļ
	39	Problems and Solutions			

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Name of Faculty	:	Neetu Bala
Discipline	:	Mathematics
Semester	:	III
Subject	:	Discrete Structures (CSE-203 E)
Lesson Plan Duration:		15 weeks
Work Load (Lecture/Practical) per week (in hours): Lectures 04 hours.		

Week		Theory	Actual Covered
	Lecture Day	Topic (Including Assignment/Test)	
4 of	1	Introduction to set theory, Set operations,	
1 st	2	Algebra of sets, Duality, Finite and Infinite sets,	
	3	Classes of sets, Power Sets, Multi sets,	
	4	Problems and solutions	
	5	Cartesian Product	
2^{nd}	6	Representation of relations , Types of relation,	
	7	Equivalence relations and partitions	
	8	Problems and Solutions	
3 rd	9	Partial ordering relations and lattices Function and its types	
	10	Composition of function and relations	
	11	Cardinality and inverse relations	
	12	Problems and Solutions	
	12	Basic operations: AND(^), OR(v), NOT(~).	
4^{th}	13	Truth value of a compound statement	
	15		
		propositions, tautologies, contradictions.	
	16	Problems and Solutions	
5th	17	Permutations with and without repetition	
Sui	18	Combination	
	19	Polynomials and their evaluation	
	20	Problems and Solutions	
	21	Sequences	
6th	22	Introduction to AP, GP and AG series, partial fractions,	
	23	partial fractions	
	24	Problems and Solutions	
7 th		Ist Minor Test	
	25	linear recurrence relation with constant coefficients	
8th	26	Homogeneous solutions, Particular solutions	
	27	Total solution of a recurrence relation using generating functions.	
	28	Problems and Solutions	
	28	Definition and examples of a monoid,	
9th	30		
<i>y</i> ui		Semigroup	
	31	Groups and rings	
	32	Problems and Solutions	
10th	33	Homomorphism,	
Tom	34	Isomorphism and Automorphism	
	35	Subgroups and Normal subgroups	
	36	Problems and Solutions	
	37	Cyclic groups	
11 th	38	Integral domain and fields	
11	39	Cosets	
	40	Problems and Solutions	
	41	Lagrange"s theorem	
12th	42	Introduction to graphs	
	43	Directed and Undirected graphs	
	44	Problems and Solutions	
13 th	45	Homomorphic and Isomorphic graphs,	
	46	Subgraphs, Cut points and Bridges	
	47	Multigraph and Weighted graph, Paths and circuits	
	48	Shortest path in weighted graphs, Eurelian path and	
		circuits2 nd Minor Test	
4th			
15.1	49	Hamilton paths and circuits,	
15th	50	Planar graphs, Euler [°] s formula	
	51	Trees, Spanning trees, Binary trees and its traversals	
	52	Problems and Solutions	

Name of Faculty	:	Mr. Pramod Lega, Assistant Professor
Discipline	:	Management
Semester	:	3rd
Subject	:	Fundamentals of Management
Lesson Plan Duration:		15 weeks (from August, 2018 to November, 2018)
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Work Load (Lecture/Practical) per week (in hours): Lectures 03 hours.

Week		Theory
	Lecture	Topic (Including Assignment/Test)
	Day	
	1	Definitions of Management
1^{st}	2	Characteristics of Management
	3	Significance, Practical Implications of Management
	4	Management- Art, Science and Profession
2^{nd}	5	Development of Management Thoughts
	6	Classical Theory
	7	Neo- Classical Approach
3 rd	8	Contingency Approach
	9	Principles of Management (Henri Fayol)
	10	Scientific Management (F.W.Taylor)
4^{th}	11	Human Relation Movement (Elton Mayo)
	12	Managerial Functions of Management
	13	Introduction of Human Resource Management
5th	14	Nature and Objectives of Human Resource Management
	15	Functions of Human Resource Management
	16	Meaning and Definitions of Human resource planning
6th	17	Recruitment, Selection
	18	Training and Development
7 th		1 st Minor Test
	19	Meaning and Definitions of Marketing Management
8th	20	Functions of Marketing Management
	21	Objectives and functions of Marketing
	22	Marketing Mix
9th	23	Process of Marketing Research
	24	Meaning and Definitions of Advertising
	25	Functions and Significance of Advertising
10th	26	Media of Advertisement
	27	Criticism of Advertisement
	28	Meaning and Definitions of Consumer Behaviour
11 th	29	Meaning and Definitions of Production Management
	30	Functions of Production Management
	31	Objectives and functions of Production Management
12th	32	Meaning and Definitions of Production Planning and Control
	33	Steps/Elements of Production Planning and Control
13 th	34	Objectives and functions of Material management
	35	Inventory Control
	36	Production Layout
14th		2 nd Minor Test
	37	Meaning and Definitions of Financial Management
15th	38	Capital Structure and various Sources of Finance,
		Working Capital, Short term and long term finances
	39	Capital Budgeting

Name of Faculty	:	Gaurav Singh Sisodia
Discipline	:	Mathematics
Semester	:	III
Subject	:	Mathematics –III (MAT-201-L)
Lesson Plan Dura	tion:	15 weeks (from August, 2018 to November, 2018)
Work Load (Lectur	e/Praction	cal) per week (in hours): Lectures 04 hours.

Week	Theory				
	Lecture	Topic (Including Assignment/Test)			
	Day				
	1	Euler's Formulae			
1^{st}	2	Dirichlet's Condition for Fourier expansions			
	3	Problems and Solutions			
	4	Fourier expansion of functions having point of discontinuity			
	5	Change of interval			
2^{nd}	6	Odd and even functions			
	7	Problems and Solutions			
	8	Fourier expansion of square wave			
	9	Rectangular wave, saw-toothed wave			
3 rd	10	Half and full rectified wave			
5	10	Half range sine and cosine series			
	11	Problems and Solutions			
	12	Fourier integrals Theorem			
4^{th}	13	Fourier transforms			
+	14	Fourier sine & cosine transforms			
	16	Properties of Fourier transforms,			
5.1	17	Convolution theorem			
5th	18	Shifting theorem (both on time and frequency axes)			
	19	Fourier transforms of derivatives			
	20	Fourier transforms of integrals			
	21	Fourier transform of Dirac delta function			
6th	22	Problems and Solutions			
	23	Functions of complex variable, limit & continuity of a function			
	24	Exponential, Trigonometric, Hyperbolic & Logarithmic functions			
$7^{\rm th}$		Ist Minor Test			
	25	Differentiability & Analyticity			
8th	26	C-R equations: necessary & sufficient condition for function to be analytic			
	27	Polar form of C-R equations, Harmonic functions			
	28	Integration of complex functions			
	29	Problems and Solutions			
9th	30	Cauchy Theorem, Cauchy- Integral formula.			
	31	Power series, radius and circle of convergence			
	32	Taylor's Maclaurin's and Laurent's series			
	33	Zeroes and singularities of complex functions			
10th	34	Residues. Evaluation of real integrals using residues (around unit circle)			
	35	Residues. Evaluation of real integrals using residues (around semi circle)			
	36	Problems and Solutions			
	37	Introduction of Probability Distributions and Hypothesis Testing			
	38	Expected value of a random variable			
11^{th}	39	Baye's Theorem			
	40	Discrete and continuous probability distribution.			
	41	Testing of a hypothesis, tests of significance for large samples			
12th	41	Properties and application of Binomial distribution.			
1201	42	Student's t-distribution (applications only)			
	43	Chi-square test of goodness of fit			
13 th	44	Problems and Solutions			
13					
	46	Linear Programming problems formulation			
	47	Solution of LPP using Graphical Method			
4 4 .4	48	Canonical and Standard form of LPP			
14th	40	2 nd Minor Test			
	49	Linear Programming problems formulation			
15th	50	Solution of LPP using Simplex Method			
	51	Solution of LPP for degeneracy problem			
	52	Solution of LPP using Dual Simplex Method			