

CURRICULUM VITAE

Name: Sita Devi

Father's Name: Sh. Ramphal Singh

Qualifications:

- B. Tech. (Electrical Engg.) from Ch. Devi Lal Memorial Government Engineering Collage, Panniwala Mota (Sirsa) affiliated to Ch. Devi Lal University, Sirsa.
- Master of Engg. (Power System) from GJUS&T, Hisar

Area of Interest:

- **power system**
- **Electrical Machine**

Experience:

- Teaching: 5 Years
- Research: None
- Industry: None

List of Publications in International/ National Journals: (with complete detail

- Published a paper titled "Review of CUK Converter for maximum power point tracking of photo voltaic system" in the International Journal (IJTR) having ISSN 2278-5787, Nov – Dec. 2015 by Om Institute of Technology & Management, Hisar
- Published a paper titled "Performance analysis of PI controller less incremental conduction based MPPT technique for PV fed standalone variable resistive load" in the International Journal (IJTR) having ISSN 2278-5787, July – Aug, 2016 by Om Institute of Technology & Management, Hisar.
- Published a paper titled “ Maximum power tracking for photovoltaic power system” in the international journal having ISSN 2347-4718, volume 5 ,10 June 2018
- Published a paper titled “Unbundling and competition in Indian electricity market” in the international journal(IJTRE) having ISSN 2347-4718,10 June 2018

Responsibilities held:

- 1) Conduct of Seminars and test series for improving students performance of students.
- 2) Mentor of students.
- 3) Teaching theory & practical subject with 16-20 hours per week.
- 4) **TPO cell member**
- 5) Member of admission committee

Achievements/ Awards, if any: 1st position in Diploma & M. Tech.

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