

# CURRICULUM VITAE

**Name:** Sita Devi

**Father's Name:** Sh. Ramphal Singh

**Qualifications:** B. Tech. (Electrical Engg.) from Ch. Devi Lal Memorial Government Engineering Collage, Panniwala Mota (Sirsa) affiliated to Ch. Devi Lal University, Sirsa.

Master of Engg. (Power System) from GJUS&T, Hisar

**Area of Interest:** Power System

**Experience:**

Teaching: 2 Years

Research: None

Industry: None

**List of Publications in International/ National Journals: (with complete detail):**

- Published a paper titled "Review of CUK Converter for maximum power point tracking of photo voltaic system" in the International Journal (IJTR) having ISSN 2278-5787, Nov – Dec. 2015 by Om Institute of Technology & Management, Hisar
- Published a paper titled "Performance analysis of PI controller less incremental conduction based MPPT technique for PV fed standalone variable resistive load" in the International Journal (IJTR) having ISSN 2278-5787, July – Aug, 2016 by Om Institute of Technology & Management, Hisar.

**Responsibilities held:**

- 1) Conduct of Seminars and test series for improving students performance of students.
- 2) Mentor of students.
- 3) Teaching theory & practical subject with 16-20 hours per week.

**Achievements/ Awards, if any:** 1<sup>st</sup> position in Diploma & M. Tech.

**PAN No.:** CQFPD4260A

**Adhaar No.:** 7870 9907 3557

**Email Id:** [Sitajhajhriya100@gmail.com](mailto:Sitajhajhriya100@gmail.com)

**Mb. No.:** 8930342313